

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q78432

Hien Boon TAN, et al.

Appln. No.: 10/721,382  
Confirmation No.: 6007

Group Art Unit: 2822  
Examiner: David E. Graybill

Filed: November 26, 2003

For: HIGH PERFORMANCE CHIP SCALE LEADFRAME PACKAGE METHOD OF  
MANUFACTURING THE PACKAGE

**PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicants hereby petition for an extension of time of three months, extending the time for responding to the Office Action of March 6, 2006 to September 6, 2006.

As mentioned in the accompanying Response to the Office Action, the Examiner has reviewed claims that have been withdrawn. After Applicants discovered the error, Applicants' attorney contacted the Examiner and requested that the Examiner issue a new, proper, Office Action. However, the Examiner requested that Applicants file a response to the outstanding Office Action and explain the error so that the Examiner could then issue a new Office Action that addressed claims 21-26.

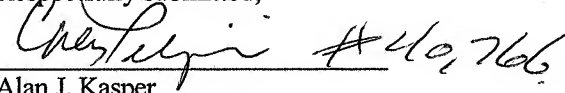
Because the error was caused by the PTO, Applicants request that they not be charged any fees, including extension fees. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this sheet is enclosed.

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WASHINGTON OFFICE  
23373  
CUSTOMER NUMBER

Date: September 5, 2006

Respectfully submitted,

  
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